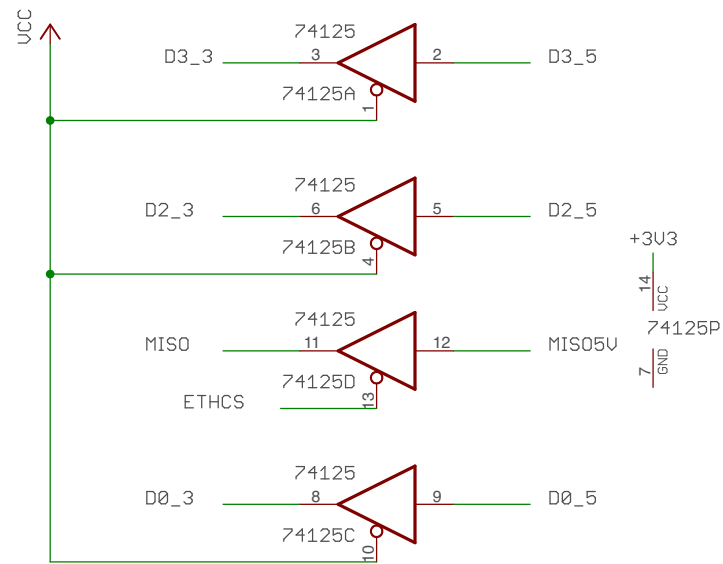
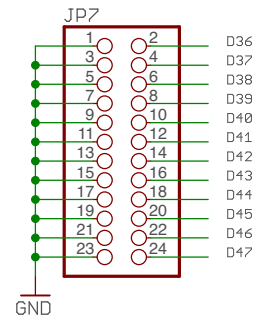
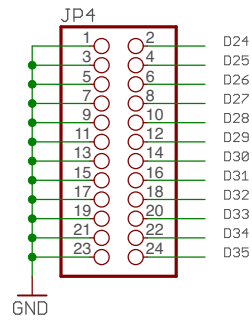
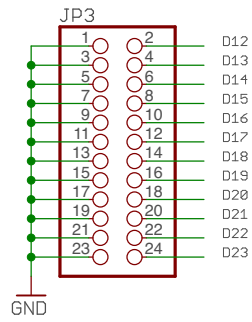
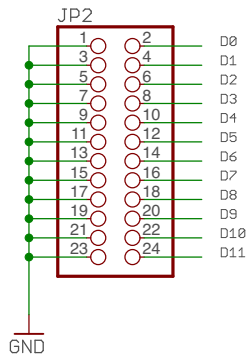


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TITLE: Ethernet_DOUT.MT

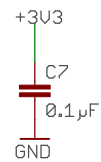
Design by: **Randall Hyde** REV:

Date: 5/9/20 11:47 PM Sheet: 1/2



Rev 2:
 Connected 3V3 (pin 26) to Feather 3.3V.
 Added 741vc125 buffer to protect MISO line.
 Redesigned serial shift register logic to use 74HC595 ICs.
 Changed LED resistor to 470Ω.

Rev 3:
 Connected Feather RST line to Teensy PROG.
 Changed serial output from QH to QH' on the 74HC595 ICs.



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TITLE: Etherport_DOUT.MT

Design by:
 Randall Hyde

REV:

Date: 5/9/20 11:47 PM

Sheet: 2/2